

VLSI PROJECTS

1. IMPLEMENTATION OF TRAFFIC LIGHT CONTROLLER
2. IMPLEMENTATION OF DUAL PORT RAM USING VHDL
3. IMPLEMENTATION OF 4-BIT DATA ENCRYPTION STANDARD (DES) ALGORITHM USING VERILOG HDL
4. IMPLEMENTATION OF UNIVERSAL ASYNCHRONOUS RECEIVER/TRANSMITTER(UART
5. IMPELMANTATION OF 7X8 DOT MATRIX DISPLAY
6. IMPLEMENTATION OF 8-BIT CONDITIONAL SUM ADDER AND PARALLEL PREFIX ADDERS FOR FAST ADDITION
7. IMPLEMENTATION OF 32-BIT PSEUDO RANDOM TEST PATTERN GENERATOR USING VHDL
8. IMPLEMENTATION OF MEALY TYPE AND MOORE TYPE SERIAL ADDERS
9. IMPLEMENTATION OF I2C CONTROLLER WITH MULTIPLE BAUDRATE GENERATOR
10. IMPLEMENTATION OF 32-BIT BARREL SHIFTER USING MICROWIND & VHDL.
11. IMPLEMENTATION OF 32-BIT BRAUN & BAUGH-WOOLEY MULTIPLIERS USING VHDL .
12. IMPLEMENTATION OF MATRIX MULTIPLICATION USING VHDL
13. IMPLEMENTATION OF CYCLIC REDUNDANCY CHECK USING VHDL IMPLEMENTATION OF 16-BIT HAMMING CODE ENCODER AND DECODER FOR SINGLE BIT ERROR DETECTOR AND CORRECTOR
14. STATIC RANDOM ACCESS MEMORY
15. DIGITAL CLOCK.
- 16.IMPLEMENTATION OF 16-BIT MULTIPLEXER AND DEMULTIPLEXER USING MICROWIND AND VHDL
17. IMPLEMENTATION OF 7-2 REDUCTION USING CARRY SAVE ADDER
18. IMPLIMENTATION OF SEQUENCE DETECTOR USING FINITE STATE MACHINES
19. IMPLEMENTATION OF THE HDLC CONTROLLER.
20. IMPLEMENTATION OF THE VGA PORT USING VHDL CODE.

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